

Abstracts

Circuit Techniques for the Noise Reduction and Frequency Stabilization of Avalanche Diode Oscillators

E.F. Scherer. "Circuit Techniques for the Noise Reduction and Frequency Stabilization of Avalanche Diode Oscillators." 1968 G-MTT International Microwave Symposium Digest and Technical Program 68.1 (1968 [MWSYM]): 63-71.

The noise performance of avalanche oscillators is limited by the inherent noisiness of the avalanche process. It is shown that by suitable system design and the use of noise reduction techniques the device limitations can be overcome in many applications.

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